



PATENT

- 1 -

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

KAMESH V. GADEPALLY

Application No. 10/006,334

Filed: December 3, 2001

For: **METHOD FOR MANUFACTURING  
AN INTEGRATED CIRCUIT  
STRUCTURE WITH LIMITED  
SOURCE SALICIDATION**

Group Art Unit: 2818

Examiner: QUOC DINH HOANG

**NOTICE OF  
APPEAL**

400 Montgomery St., Suite 1110  
San Francisco, CA 94104  
(415) 433-2250

Mail Stop AF  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on October 21, 2003.

GIRARD & EQUITZ LLP Date: 10/21/03

By: Marsha Ann Townsend  
Marsha Ann Townsend

Sir:

Applicant hereby appeals to the Board of Appeals and Interferences from the decision dated September 17, 2003, of the Primary Examiner finally rejecting claims 1-13 of the referenced application.

A check for the appeal fee of \$330.00 is enclosed. The Commissioner is hereby authorized to charge our Deposit Account No. 50-1697 for any further fees in regard to this patent application. A duplicate copy of this Notice is enclosed for this purpose.

Respectfully submitted,  
GIRARD & EQUITZ LLP

Dated: 10/21/03

By: Alfred A. Equitz

Alfred A. Equitz  
Reg. No. 30,922

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Attorneys for Applicant(s)

Attorney Docket No. NSC1-G0610 [P04402 P01]